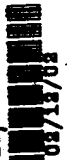


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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10072872	FILING DATE 02/12/2002	CLASS 327	SUBCLASS 57	GAU 2847	EXAMINER Long Nguyen
**APPLICANTS: Kanamori Yoshie; Takauchi Hideki; Ishida Hideki;					
**CONTINUING DATA VERIFIED: <i>None</i>					
** FOREIGN APPLICATIONS VERIFIED: JAPAN 2001-145298 05/15/2001 <i>Verified</i>					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials <i>2</i>				ATTORNEY DOCKET NO 100021-00069	
TITLE : Differential amplifier circuit capable of accurately amplifying even high-speeded signal of small amplitude					

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
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